

Docket No.: WMP-SME 396

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : LUDWIG LEIPOLD ET AL. ET AL.

Filed : CONCURRENTLY HEREWITH 11/29/01

Title : METHOD AND TIMING CIRCUIT FOR GENERATING A
SWITCHING OR CONTROL SIGNAL

146
105
JC978 U.S. PTO
09/997984
11/29/01

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

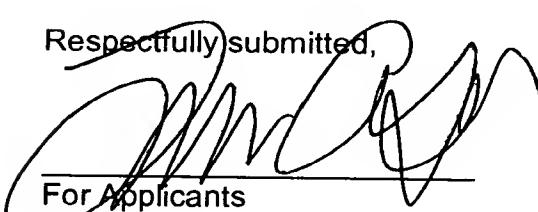
Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications
are submitted herewith:

U.K. Patent Application GB 2 141 483 A (Bodig et al.), dated December 19, 1984;

International Search Report, dated February 1, 2001;

Respectfully submitted,


For Applicants

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Date: November 29, 2001

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